

ŧ)

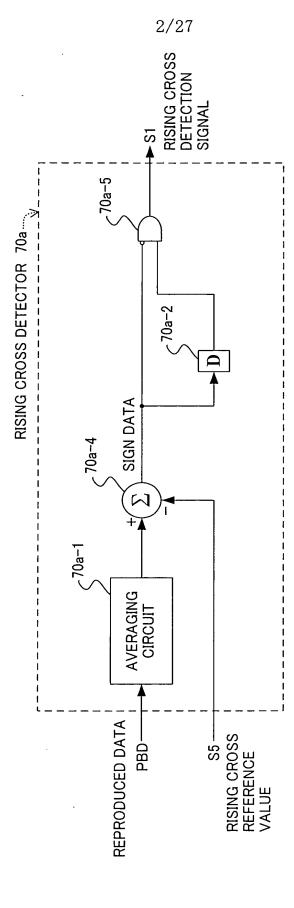


FIG. 2

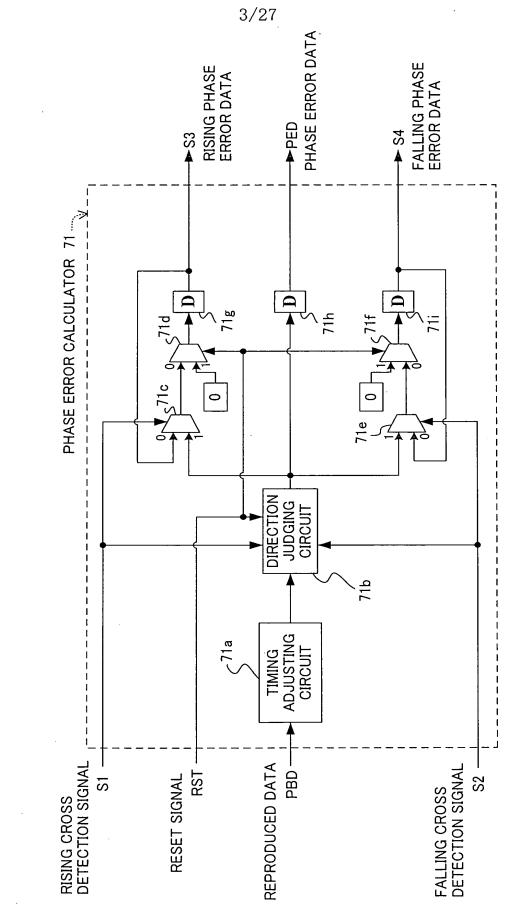


FIG.

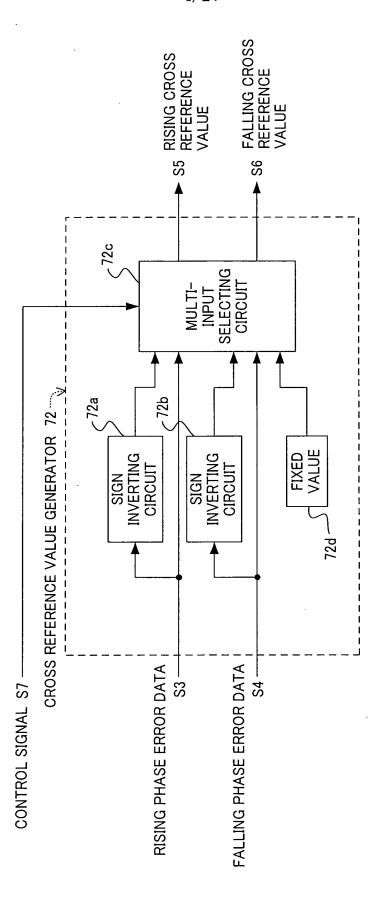


FIG. 4



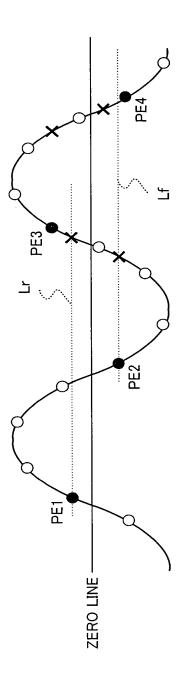
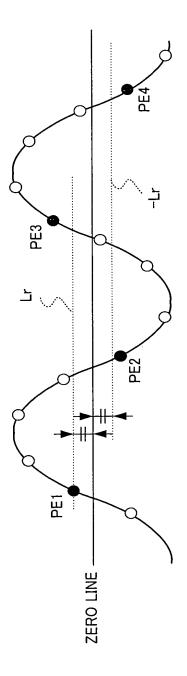


FIG. 6



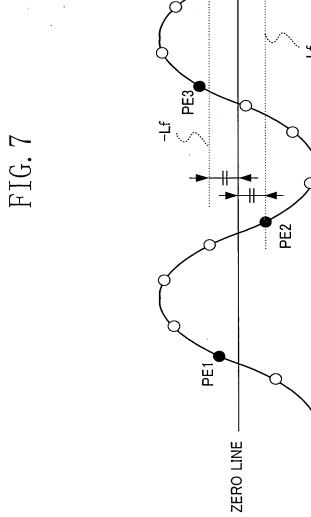
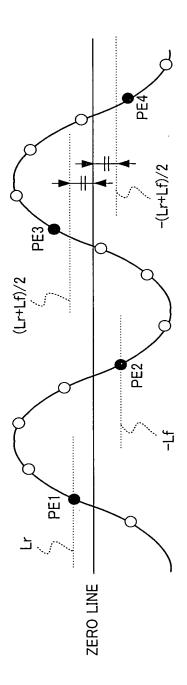


FIG. 8



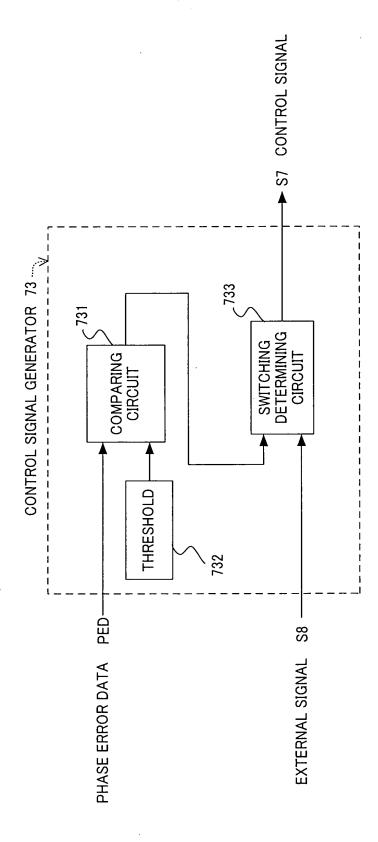


FIG. 9

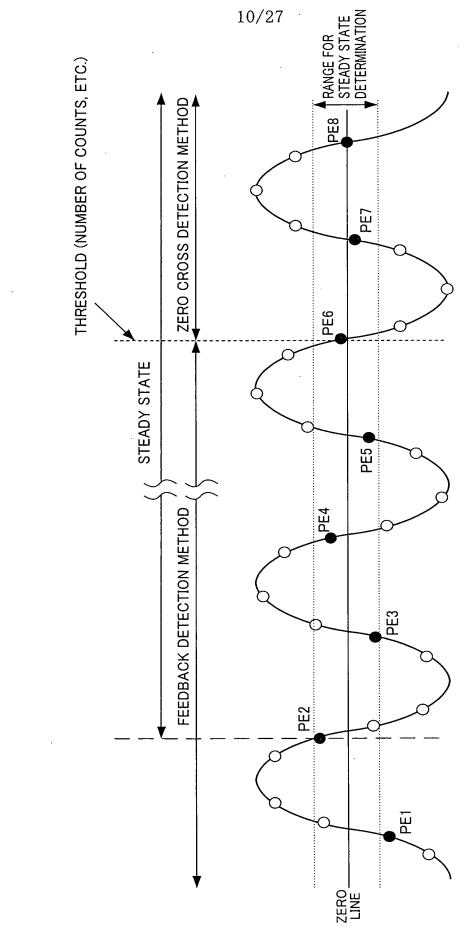
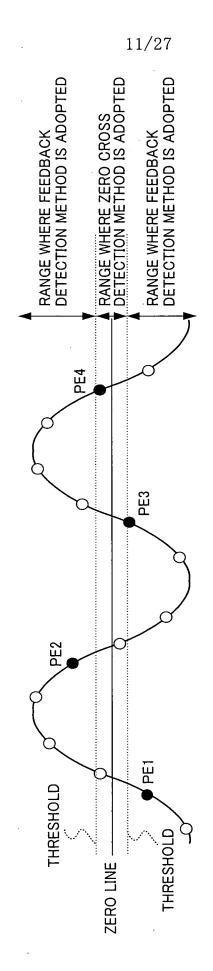
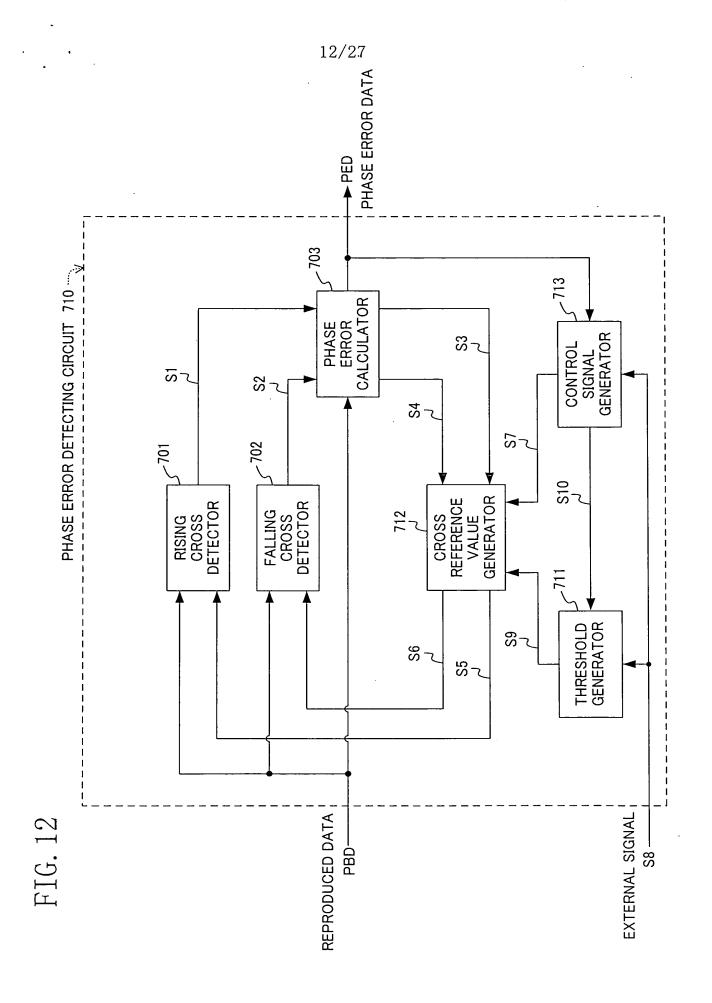


FIG. 10







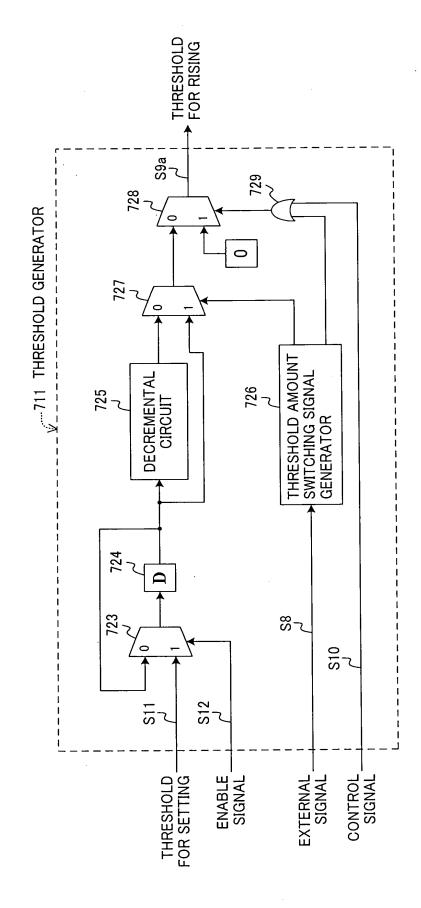
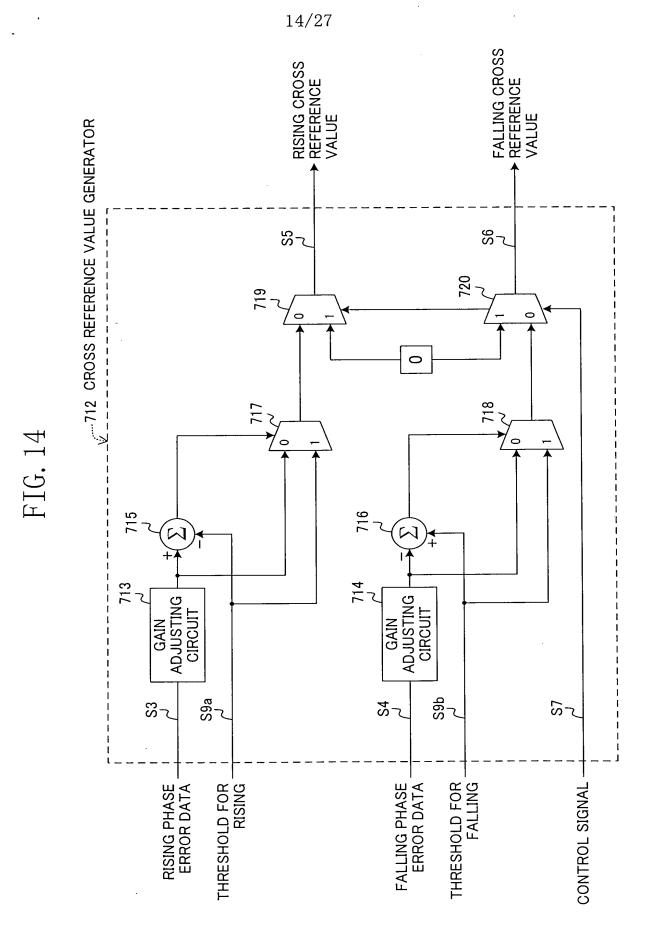


FIG. 13



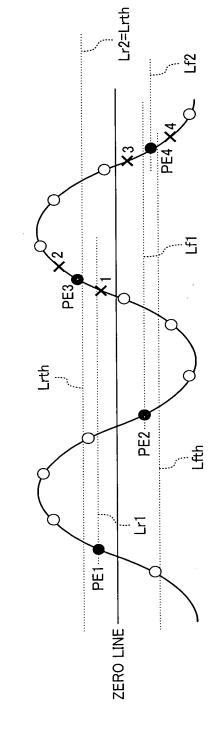
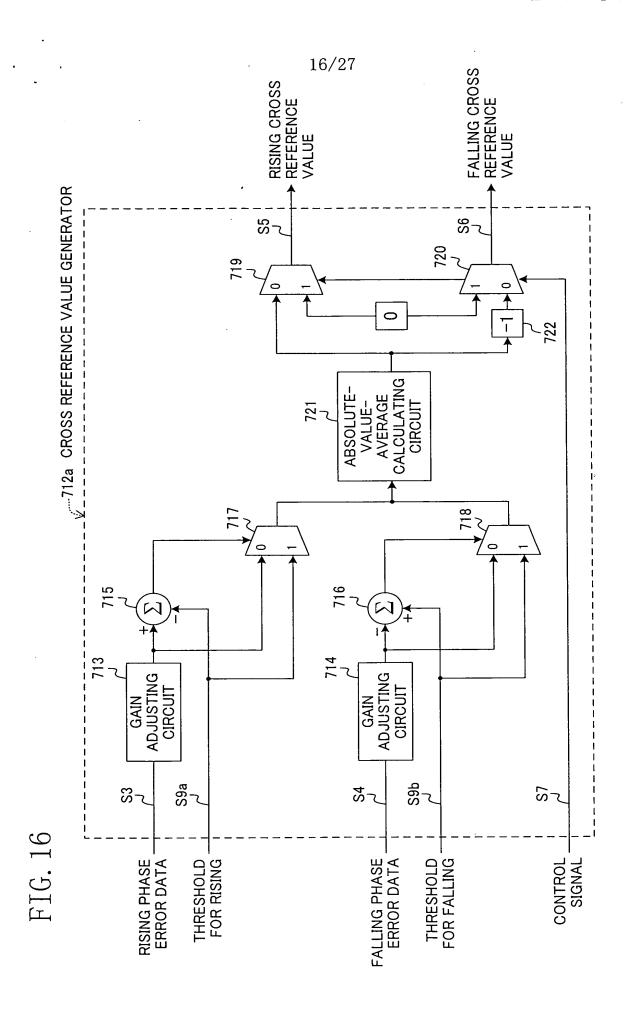


FIG. 15



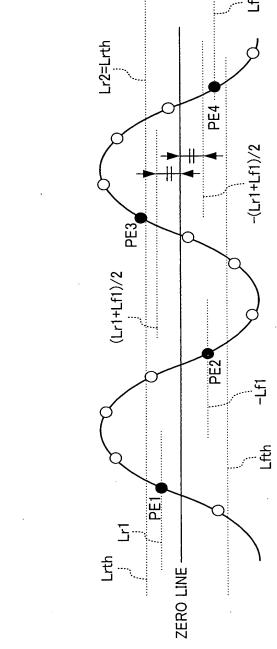


FIG. 17

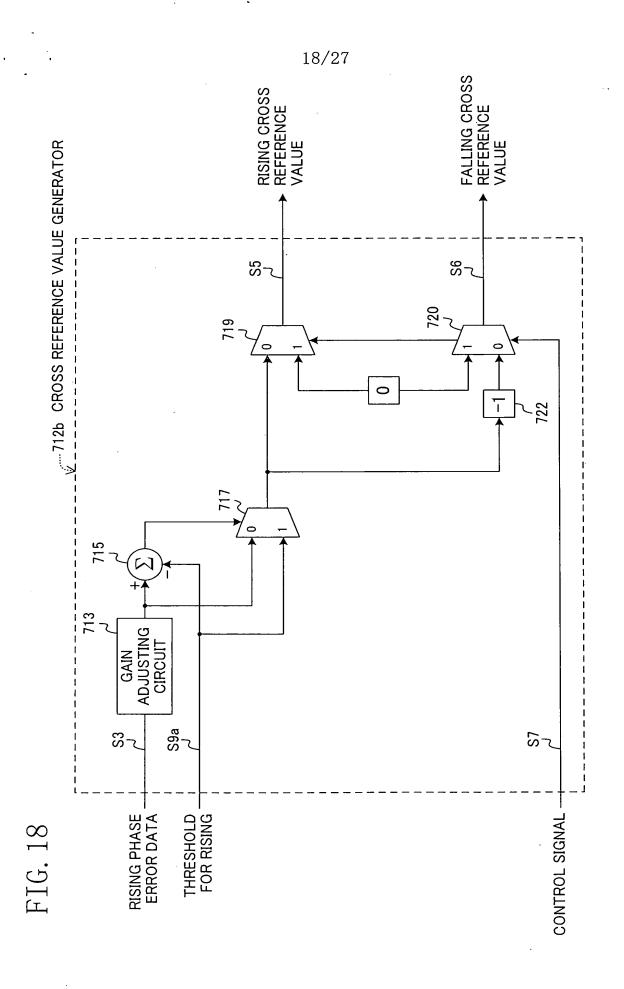
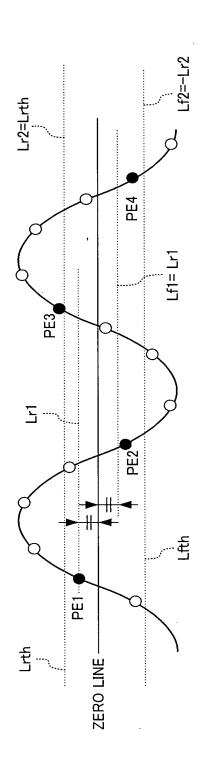


FIG. 19



Lr1=-Lf1

FIG. 20

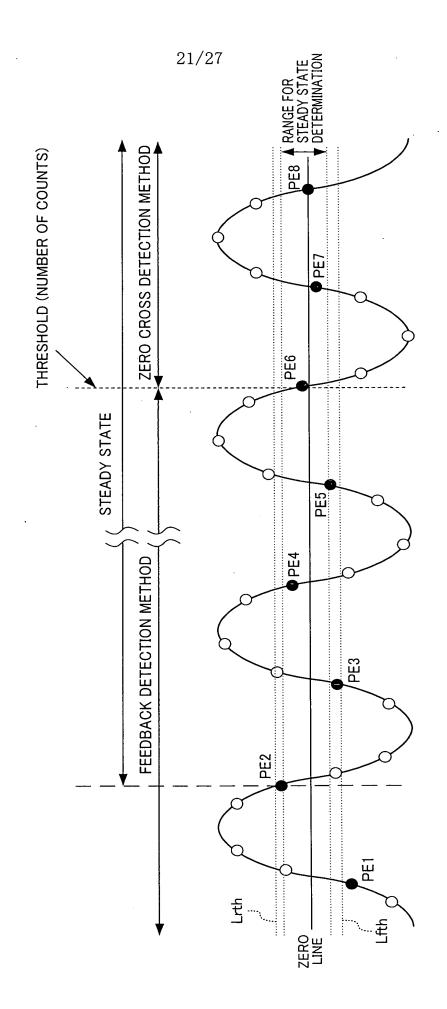


FIG. 21

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FIG. 22

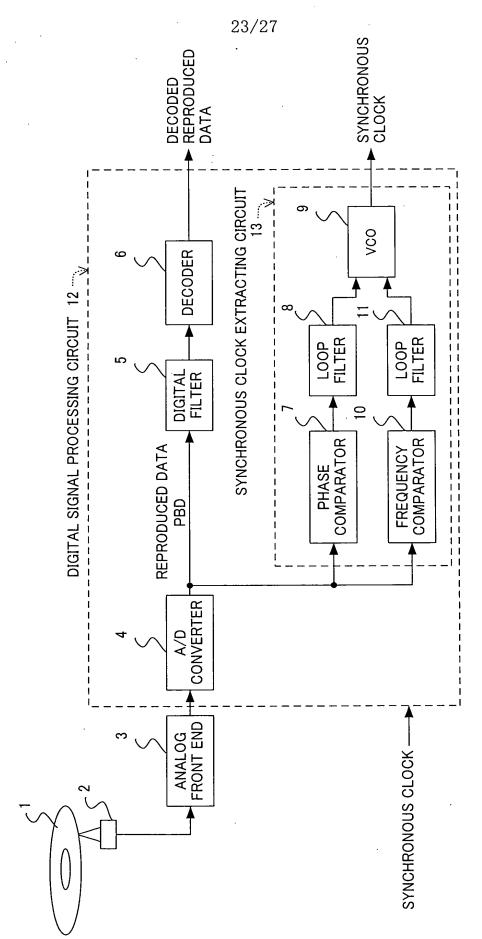


FIG. 23

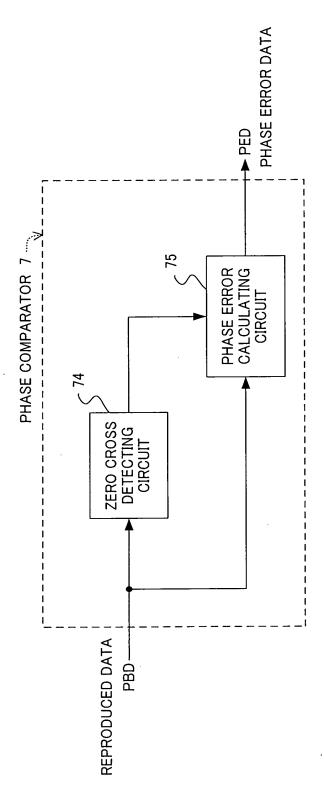
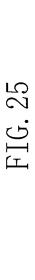
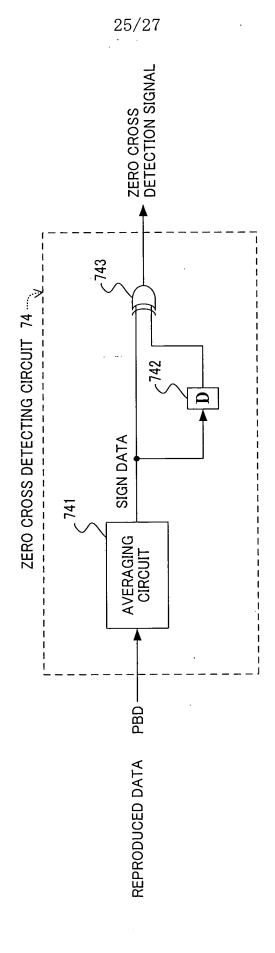
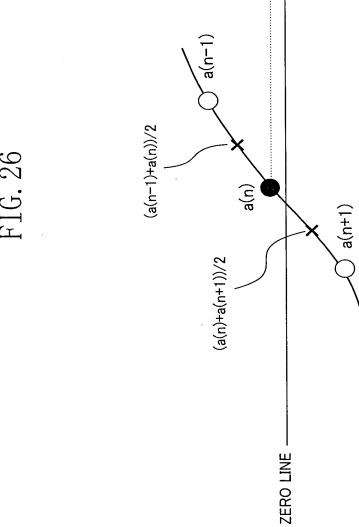


FIG. 24





PHASE ERROR



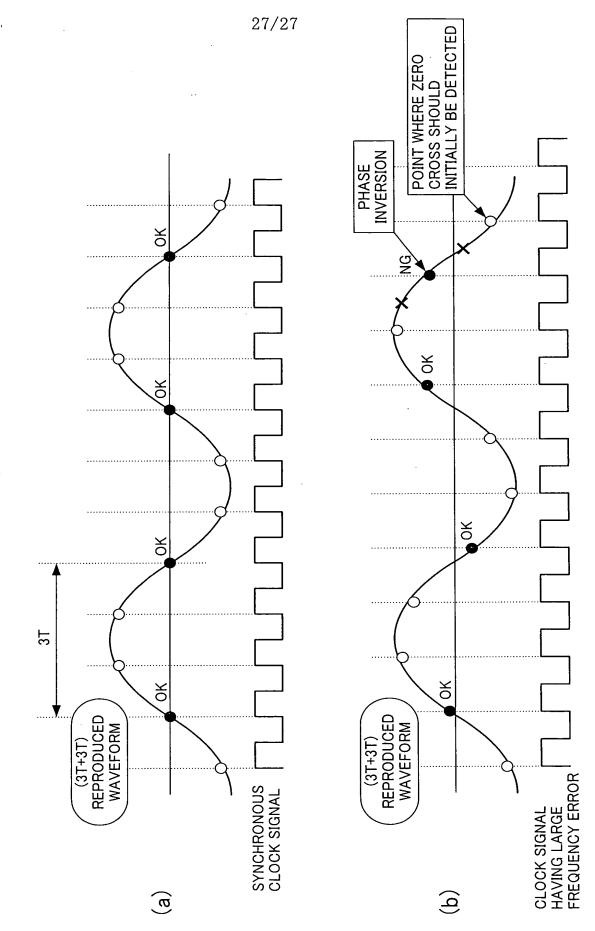


FIG. 27